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Applicant : Ryo Inoue et al.  
Serial No. : 09/745,104  
Filed : December 20, 2000Art Unit : 2183  
Examiner : Charles A. HarknessTitle : Hardware Loops and A Pipeline System Using Advanced Generation of Loop  
Parameters (As Amended)Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is Request for Continued  
Examination (RCE), faxed this 2nd day of September, 2004, to the United States Patent and  
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Respectfully submitted,

  
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